

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	54108	memory adj controller	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:41
L2	317	1 same (clock near3 (select\$3 choos\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:42
L3	30	1 same (clock near3 (select\$3 choos\$3)) with (mux multiplex\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:57
L4	1160	(second adj clock) with (mux multiplex\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:57
L5	438	4 with (select\$3 choos\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:58
L6	3	5 same (memory adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:58
L7	3	5 same ((dram sdram ddr memory) adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:59
L8	2880	(provid\$3 suppl\$4) near6 (second adj clock)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:59
L9	100	8 with selective\$2	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:59
L10	0	9 same ((dram sdram ddr memory) adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:00
L11	392	(clock near5 selective\$2) with (mux multiplex\$2)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:00
L12	4	11 same ((dram sdram ddr memory) adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:03
L13	58860	(switch\$3 chang\$3 select\$3 choos\$3) near3 clock	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:03
L14	647	13 same ((dram sdram ddr memory) adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:04
L15	279	13 with ((dram sdram ddr memory) adj2 (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:04

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L16	18	15 same (mux multiplex\$2)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 12:04
L17	4	("3564507" "5535377" "5884100"). PN. OR ("6603828").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/19 15:04
L18	8757	(multiplex\$2 mux) near3 (select control) with (register flop latch)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 15:08
L19	1209	(register flop latch) near3 (hold\$3 contain\$3 stor\$3) near5 (select control) with (mux multiplex\$2)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 15:09
L20	804	(register flop latch) near3 (hold\$3 contain\$3 stor\$3) near5 (select control) near6 (mux multiplex\$2)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 15:07
L21	22	19 same (mux multiplex\$2) near3 program\$5	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 15:09
L22	16	19 same (clock near3 (select\$3 choos\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 15:09
S1	25	(HUNG near2 ERIC).in. (DESAI near2 GEETA).in. (KUROODI near2 VIJENDRA).in. (MIRETSKY near2 ALEXANDER).in. (VOJNOVIC near2 MIRKO).in.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:09
S2	3	("5,260,892" "5,856,937" "6,381, 164").pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:13
S3	3	("20030107908" "5,945,886" "20030137860").pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:17
S4	322	(memory adj (controller interface)) near3 ddr	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:21
S5	0	S4 with (combin\$3 integrat\$3) near3 (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:17
S6	1103	713/500.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:31
S7	708	(memory adj (controller interface)) with (combin\$3 integrat\$3) near3 (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:26
S8	136	S7 with (die chip)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:22

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S9	10	S7 same ddr	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:25
S10	4	(memory adj (controller interface)) near3 (slower ((lower less reduc\$3) near3 (clock frequency))) near6 (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:39
S11	6	("5,448,715" "4,615,017" "6,076,170" "6,327,667" "6,516,362" "6,134,638"). pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 11:39
S12	49	("5448715").URPN.	USPAT	OR	OFF	2007/01/03 11:48
S13	4	("6,941,416" "6,366,989" "7,130,932" "6,470,376").pn.	USPAT	OR	OFF	2007/01/03 11:51
S14	22	("3828325" "5146592" "5239639" "5473572" "5548735" "5740466" "5829035" "5941968" "5956349" "6134638").PN. OR ("6366989"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/01/03 13:26
S15	3	(cpu processor) near3 (faster higher greater) near3 (clock frequency) with (memory adj (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:05
S16	0	(cpu processor) near3 (faster higher greater) near3 (clock frequency) with (ddr adj (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 13:30
S17	2	(cpu processor) near3 (faster higher greater) near3 (clock frequency) with ((dram sdram) adj (controller interface))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 13:31
S18	905	(memory adj (controller interface)) with (ddr (double adj data))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 13:34
S19	46	S18 and ((cpu processor) near3 (together integrat\$3 combin\$3) near6 (interface controller))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 13:35
S20	26	S19 and ((controller interface) near3 (speed frequency clock))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:03
S21	3	S20 and mhz	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 13:38
S22	4814	(cpu processor) near3 (higher greater increas\$2) near3 (speed frequency clock)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:06
S23	11	S22.with (memory adj2 (interface controller))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:09

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S24	0	S23 and (ddr (double adj data))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:06
S25	80	(memory adj2 (interface controller)) near3 (slower lower less\$2 reduc\$2) near6 (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:10
S26	4	S25 and (ddr (double adj data))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:11
S27	955	(memory adj2 (interface controller)) with (ddr (double adj data))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:11
S28	306	S27 same (processor cpu)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:11
S29	193	S28 and ((cpu processor) same (controller interface) same (mhz speed frequency))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:12
S30	23	S29 and ((controller interface) near3 ("with" together combin\$2 integrat\$3 chip die) near6 (cpu processor))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:22
S31	22710	(memory adj (interface controller)) with (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:23
S32	111	S31 with (pcb (circuit adj (board card)))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:24
S33	4	S32 and ((pcb (circuit adj (board card))) with (layer\$1 near3 two))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:26
S34	564	(pcb (circuit adj (card board))) with (layer\$1 near3 (reduc\$4 decreas\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:26
S35	348	(pcb (circuit adj (card board))) near6 (layer\$1 near3 (reduc\$4 decreas\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:26
S36	24	S35 and (memory adj (controller interface)) and (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:34
S37	832	(memory adj (interface controller)) with (processor cpu) with (board card pcb)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:41
S38	7	S37 and ((board pcb card) near3 layer\$1 near3 (two reduc\$4 fewer lesser decreas\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:43

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S39	2610	(pcb (circuit adj (board card))) with (layer\$1 near3 two)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:44
S40	1404	(pcb (circuit adj (board card))) near3 (layer\$1 near3 two)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:46
S41	4	S40 and (layer\$1 near3 (fewer less reduc\$4 decreas\$3) near3 four)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:45
S42	358	S40 and (layer\$1 near3 (fewer less reduc\$4 decreas\$3))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:45
S43	0	(pcb (circuit adj (board card))) near3 (layer\$1 near3 two) near3 maximum	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:46
S44	150537	two adj layer\$1	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:46
S45	1123	two adj layer\$1 with (pcb (circuit adj (board card)))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:47
S46	1123	(two adj layer\$1) with (pcb (circuit adj (board card)))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:47
S47	43	((two adj layer\$1) near3 only) with (pcb (circuit adj (board card)))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 14:47
S48	161	pcb near5 (two adj layer\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:14
S49	1	S48 with improve\$4	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:17
S50	251	pcb with (two adj layer\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:21
S51	384247	(minimiz\$5 reduc\$4 decreas\$3) near3 (cost complexity)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:19
S52	4	S50 same S51	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:19
S53	930	(circuit adj (board card)) with (two adj layer\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:21

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S54	13	S51 same S53	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:22
S55	2	S51 with S53	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:22
S56	0	"5686764.on"	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:31
S57	1	"5686764".pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/03 15:32
S58	54108	memory adj controller	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:20
S59	47	S58 near6 (second adj clock)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:22
S60	0	S59 with (mux multiplexer)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:22
S61	11148	clock near6 (mux multiplexer)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:23
S62	11148	clock near6 (mux multiplexer)S61 with S58	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:23
S63	38	S61 with S58	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:31
S64	137	(clock near3 (select\$3 choos\$3)) with S58	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:32
S65	5	S64 same (clock near3 (mux multiplexer))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:43
S66	18861	S58 with (cpu processor ("execution unit"))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:44
S67	1038	S66 same (pcb board motherboard)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:45
S68	14830	S58 near6 (cpu processor ("execution unit"))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:45

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S69	808	S68 same (pcb board motherboard)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:46
S70	3032	S58 near6 (cpu processor ("execution unit")) with (combin\$3 integrat\$3 ("same" adj (die chip)))	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 14:45
S71	109	S70 same (pcb board motherboard)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 15:26
S72	0	ddr near3 faster near3 (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 15:27
S73	1	ddr near3 faster with (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 15:27
S74	1816	(memory ram sdram dram) near3 faster with (cpu processor)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 15:28
S75	6	S74 same ddr	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:07
S76	241	S58 near3 ddr	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:07
S77	77	S58 near3 (double adj data)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:08
S78	5	S77 with clock	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:28
S79	1899	(memory adj controller) with (cpu processor) same (motherboard pcb board card)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:28
S80	151	(memory adj controller) with (cpu processor) with mount\$3	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 16:29
S81	101	(memory adj controller) with (cpu processor) with mount\$3 with (pcb motherboard board card)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 18:57
S82	37984	(ic "integrated circuit") near6 packag\$3	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 18:58
S83	373	S82 same tsop	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 18:58

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S84	154	S83 and (wire near3 bond\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/18 19:04
S85	1	"5960539".pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/01/19 11:41